Short Term Course on
DSP APPLICATIONS USING VLSI

Time	Topic / Event	Remarks
Day-1	New registrations if any, distribution of course material, Inauguration, **finalization of Time table**, overview of course activities.	Venue - DSP Lab.
Day-2	Design using VHDL, Simulation and Synthesis	
Day-3	Practical on above topic	
Day-4	Architecture of CPLD, Architecture of FPGA	
Day-5	Practical on above topic	
Day-6	Design of Digital IIR Filters	
Day-7	Practical on above topic using VLSI Tools	
Day-8	Design of Digital FIR Filters	
Day-9	Practical on above topic using VLSI Tools	
Day-10	Architecture of floating point processors, Assembler Linker, Complier, Linux Utilities	
Day-11	CPLD minimum system design, components distribution (PCB, XC9572XL-10, power supply etc.) (Free to every participant ---do it your self and carry home)	
Day-12	Practical on above topics, Mini Project allotment. (Individual work for minimum system design and two participants per batch for project implementation)	
Day-13	Transformations, Numerical Integration Techniques, Dynamical System Model.	
Day-14	Project demonstration and certificate distribution	