



SARDAR PATEL INSTITUTE OF TECHNOLOGY

Munshi Nagar, Andheri (W) Mumbai-400 058
ELECTRONICS ENGINEERING DEPARTMENT

Room No. 305

ACADEMIC YEAR: 2017-18

TERM: II

SEMESTER: VIII

CLASS: BE (ETRX)

Time	Monday	Tuesday	Wednesday	Thursday	Friday																
8.00 – 9.00	CVD SSR	CVD SSR	CVD SSR	CVD SSR																	
9.00 – 10.00	MEMS PVK	ANT GK	MEMS PVK	ANT GK	# SDP A																
10.00 – 11.00	MEMS PVK	ANT GK	MEMS PVK	ANT GK																	
11.00 – 11.15	S H O R T B R E A K																				
11.15 – 12.15	BME KK	MCOM DCK	ROBO PF	BME KK	MCOM MRB	ROBO PF	MEM S A PVK	CVD B MRB	# SD P C	MCO M D DCK	ANT E GK	CVD A MRB	# SDP B	BME C KK	ANT D GK	MEMS E PVK	ANT A GK	MEMS B PVK	CVD C MRB	# SDP D	ROB O E PF
12.15 – 1.15	BME KK	MCOM DCK	ROBO PF	BME KK	MCOM MRB	ROBO PF															
1.15– 2.00	L U N C H																				
2.00 – 3.00	BME A KK	ANT B GK	MEMS C PVK	CVD D PHS	# SDP E	Project	Project	Project	Project												
3.00 – 4.00																					
4.00 – 5.00																					
5.00 – 6.00																					

Initial	Faculty Name
SSR	Dr. .S. S. Rathod
DCK	Dr. .D. C. Karia
PVK	Prof.P. V. Kasambe
PHS	Prof. Payal Shah
MRB	Prof. Manisha Bansode

Initial	Faculty Name
PF	Prof. Pawankumar Fakatkar
GK	Prof. Govind Kore
KK	Prof. Kumar Khandagale

Head of Dept.

Principal