



Bharatiya Vidya Bhavan's
SARDAR PATEL INSTITUTE OF TECHNOLOGY

Munshi Nagar, Andheri (W) Mumbai-400 058
ELECTRONICS ENGINEERING DEPARTMENT

Room No. 307

ACADEMIC YEAR: 2017-18

TERM: II

SEMESTER: VI

CLASS: TE (ETRX)

Time	Monday					Tuesday					Wednesday					Thursday					Friday																													
8.00 – 9.00						AIS					MITM					MITM					AIS																													
9.00 – 10.00	BVLSI					NG					DCK					DCK					KK																													
10.00 – 11.00	NAB					MP-II A SSR	VLSI B NAB	DSPP C KTT	AIS D KK	PE E RGS	AIS A KK	PE B RGS	MP-II C PD	VLSI D NAB	DSPP E KTT	DSPP					KTT					PE-I					RGS																			
10.00 – 11.00	KTT															PE-I					RGS					PE-I					RGS																			
11.00 – 11.15	S					H					O					R					T					B					R					E					A					K				
11.15 – 12.15	VLSI A NAB	DSPP B KTT	AIS C PD	PE D RGS	MP-II E PVK	DSPP A KTT	AIS B PD	PE C RGS	MP-II D SSR	VLSI E NAB	DSPP					KTT					CO					PD					BVLSI					NAB														
12.15 – 1.15											CO					PD					BVLSI					NAB					CO					PD														
1.15– 2.00	L					U					N					C					H																													
2.00 – 3.00	PE-I					RGS					DSPP					KTT					BVLSI					NAB																								
3.00 – 4.00	AIS					NG					CO					PD					AIS					KK					PE A RGS	MP-II B PF	VLSI C NAB	DSPP D KTT	AIS E PD	MP-II A	MP-II B	MP-II C	MP-II D	MP-II E										
4.00 – 5.00																																																		
5.00 – 6.00																																																		

Initial	Faculty Name
SSR	Dr. .S. S. Rathod
RGS	Dr. R.G.Sutar
DCK	Dr. D.C. Karia
KTT	Prof. K.T.Talele
NAB	Prof. N.A.Bhagat

Initial	Faculty Name
PVK	Prof. P. V. Kasambe
PD	Prof. Priya Deshpande
NG	Prof. Najib Ghatte
PF	Prof. Pawankumar Fakatkar
KK	Prof. Kumar Khandagale

Head of Dept.

Principal