



Bharatiya Vidya Bhavan's  
**SARDAR PATEL INSTITUTE OF TECHNOLOGY**  
 (Autonomous Institute Affiliated to Mumbai University)  
 Munshi Nagar, Andheri (W) Mumbai-400 058  
**ELECTRONICS ENGINEERING DEPARTMENT**

Room No. 305

ACADEMIC YEAR: 2018-19

TERM: I

SEMESTER: VII

CLASS: BE (ETRX)

Time	Monday					Tuesday					Wednesday					Thursday					Friday				
9.00 – 10.00	CCN DCK					CCN DCK					OFC E MRB 504	ICT C PVK 303	CCN A DCK 205	PE-II B NP 311	ESD D KK 301	AI A KTT 205	ICT B JR 303	CCN D DCK 205	PE-II E RGS 311	ESD C KK 301	CCN DCK				
10.00 – 11.00	ESD KK					ESD KK															CCN DCK				
11.00 – 11.15	<i>S H O R T</i>					<i>B R E A K</i>																			
11.15 – 12.15	PE-II RGS					ICT PVK					AI KTT	OFC MRB				AI RGS	OFC MRB				PE-II NP				
12.15 – 1.15	PE-II RGS					ICT PVK					AI KTT	OFC MRB				AI RGS	OFC MRB				ESD KK				
1.15– 2.00	<i>L U N C H</i>																								
2.00 – 3.00	AI D RGS 205	ICT E JR 303	CCN C DCK 205	PE-II A NP 311	ESD B KK 301	AI B KTT 205	ICT D JR 303	CCN E DCK 205	PE-II C RGS 311	ESD A KK 301	ICT JR					ESD KK					AI C KTT 205	ICT A PVK 303	CCN B DCK 205	PE-II D RGS 311	ESD E KK 301
3.00 – 4.00											PE-II NP					ICT JR									
4.00 – 5.00	BE PROJECT-I																								
5.00 – 6.00																									

Initial	Faculty Name
RGS	Dr. R.G. Sutar
DCK	Dr. .D. C. Karia
KTT	Prof. K.T.Talele
PVK	Prof. P.V. Kasambe
MRB	Prof. Manisha Bansode

Initial	Faculty Name
JR	Prof. Jayashree Rathod
KK	Prof. Kumar Khandagale
MK	ProF. Minal Khobragade
NP	Prof. Nikunj Parikh

**Head of Dept.**

**Principal**