



Prof. Manisha Ravindra Bansode

Designation (e.g. Assistant Professor), Electronics Department,
Sardar Patel Institute of Technology

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- **Date of joining the present institute:** 22nd January 2009
- **Date of joining the present department:** 22nd January 2009

- **Scholastic record:**

S.No	Degree	Institute	Year of passing	Class/CGPI/percentage
1	Ph.D in (Electronics Engineering) pursuing	Sardar Patel Institute of Technology, Andheri(W), Mumbai	pursuing	
2	M.E. (Electronics and Telecommunication Engineering)	Sardar Patel Institute of Technology, Andheri(W), Mumbai	2014	First class
3	B.E. (Electronics Engineering)	Rajarambapu Institute of Technology, Sakhrale, Sangali	2003	First class
4	H.S.C	Yashwantrao Chavan College of Science, Karad	1999	First class

- **Teaching/Industry experience in totality :** 16 years

- **Domain expertise and areas of interest:**
Communication Engineering, Electronics devices, Electromagnetic Engineering

▪ **Details of subjects taught:**

S.No.	Name of subject	Year and branch
1.	Fundamentals of communication engineering	S.E Electronics Engineering
2.	Electronic Devices	S.E Electronics Engineering
3.	Analog electronics-I	S.E Electronics Engineering
4.	Digital communication	T.E Electronics Engineering
5.	Computer networks	B.E Electronics Engineering
6.	Optical fiber communication	B.E Electronics Engineering
7.	Mobile communication,	B.E Electronics Engineering
8.	Image processing and applications	B.E Electronics Engineering

▪ **Workshops/Refresher courses/Training programs/Seminars attended:**

- Three Days workshop on “**Satellite & Digital TV**” organized by Department of Electronics & Telecommunication Engineering, Mukesh Patel School of Technology Vileparle (W), Mumbai, during 8th May to 10th May 2010.
- Two Week ISTE workshop on “**Basic Electronics**” conducted by IIT Bombay, during 28th June to 8th July 2011.
- One Day Course on “**Technical Paper presentation**” organized by Sardar Patel Institute of Technology, Andheri (W) July 2011.
- Two Day Training on “**Mentor Graphics VLSI Tools**” organized by Sardar Patel Institute of Technology, Andheri (W) July 2011.
- One Day Tutorial on “**Circuit Simulation and Real Time Circuit Simulation**” at International Conference on Communication, Information and Computing Technology” organized by Sardar Patel Institute of Technology, Andheri (W), 18th October 2012.
- One Week STTP workshop on “**NS-2: A Helicopter View**” conducted by K. J. Somaiya Institute of Engineering and Information Technology, Sion, Mumbai, during 5th Nov. to 9th Nov. 2012.
- One Week CDEEP course on “**Network Security**” conducted by S.P.I.T and IIT Bombay, during 7th Aug to 15th Nov. 2012.
- Two Week ISTE workshop on “**Analog Electronics**” conducted by IIT Kharagpur, during 4th June to 14th June 2013.
- One Day “**Engineering and Science Innovation Summit**” conducted by National Instruments, 21st August 2014.
- One Week AICTE (under QIP) course on “**RF Circuit Design**” organized by Department of Electrical Engineering, VJTI, during 9th Feb to 13th Feb 2015.

- Two week AICTE approved FDP on **Electronic System Design: from Devices to Applications** from 4th May to 15th May 2015
- Two week ISTE approved STTP conducted by IITB on **Technical Communication for scientists and engineers** from 8th Oct to 5th Dec 2015.
- One week STTP conducted by IITB on **Educational technology for engineering teachers** from 7th Jan to 7th Mar 2016.
- Two week ISTE approved STTP conducted by IITKGP on ISTE approved STTP under IITKGP on "**CMOS Mixed Signal and Radio Frequency VLSI Design**" on 26th December to 4th February 2017. (Equivalent to two week)
- Two days AICTE Sponsored National seminar on **technologies for development of Rural Areas** at Sardar Patel Institute of Technology from 27th & 28th 2017.
- Six weeks credit course on **Research Methodology and Numerical Techniques and applied research** from 13th November to 22nd December 2017.
- One week AICTE-ISTE Induction /Refresher Programme on **Innovative Teaching Learning Practices to Achieve Outcome Based Education and Accreditation** from 3rd May to 9th May 2018.
- Two days Workshop on '**Flipped learning**' at Bhavan's College Andheri (W) from 17th & 18th October, 2018.
- One week AICTE-ISTE Approved One Week STTP under Career Advancement Scheme (CAS) Hands on **Machine Learning and Deep Learning** from 13th to 18th May, 2018.
- One day session on **Sequel App** at Sardar Patel Institute of Technology, Andheri (W) on 27th July 2018.
- **Membership of professional bodies:** I am ISTE lifetime member.

▪ **Publications:**

International Conferences

S.No	Publisher	Title of International conference	Title of paper/article/poster presentation	Schedule and venue of conference
1.	Prathmesh Limaye, Sagar Khuteja, Rohan Mestri, Manisha Bansode	International Conference of Artificial Intelligence and Information Technology	Analysis of Feature Extraction and Classification Models for Lip-Reading	May 2019
2.	Pooja Babaji Said, Manisha Bansode, Dr. S.S. Rathod, Payal Shah	International Conference on Electronics, Communication, and Aerospace technology	Proportionate Study of Neuron Models for Implementation of Neural Encoder	May 2019
3.	Shraddha Mayekar, Manisha Bansode, S. S. Rathod, Manisha Bansode	International Conference on Intelligent Computing and Control	An Automated Antenna Pattern Measurement System	June 2017
4.	Ashish Mohanan, Sheryl Tuscano, Raj Shah, Manisha Bansode	3rd International Conference on Electronics & Computing Technologies	Smart Attendance System	March 10-11,2016.

▪ **Details of National and International Journal publications:**

S.No	Title of Journal	Title of paper	Co-authors
1.	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering	Ensuring Fast and Safe train Operation with advancement using PLC interlocking	Nihal S. Raut, Prafulla Solkar, Akash Yadav, Manisha Bansode
2.	International Journal of Recent Technology and Engineering	Reduction of Signal Integrity Issues in Printed Circuit Board using Electromagnetics Bandgap Structure	Fahad Bilal, Manisha Bansode, Pramod Bhavarthe, Surendra Rathod,
3.	International Journal of Computer Engineering and Applications	Low profile UHF RFID tag antenna	Juhi Patil, Manisha Bansode
4.	3rd International Conference on Electronics & Computing Technologies	Smart Attendance System	Ashish Mohanan, Sheryl Tuscano, Raj Shah, Manisha Bansode
5.	Progress in Science and Engineering Research Journal	A Novel Approach for Improving Throughput of WSN by Access Protocol	M. R. Bansode and M.M.Parmar

▪ **Other Contributions:**

- I have been working as timetable in-charge in Electronics Department since 2012-13.
- I worked as faculty co-ordinator for ABL-1 activity based on Building Automation, Fire Safety and Electronic Security 2019-20.
- I worked as faculty co-ordinator for ABL-2 activity based on Occupational Safety & Legal Studies for Engineers 2018-19.
- I worked as faculty co-ordinator for ABL-4 activity based on Technical Paper and Patent Drafting 2018-19.