



Narendra Anant Bhagat

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Sardar Patel Institute of Technology

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- **Date of joining the present institute: 07/08/2000**
- **Date of joining the present department: 07/08/2000**
- **Scholastic record: (*Begin with the highest degree and end with XII*)**

S.No	Degree	Institute	Year of passing	Class/CGPI/percentage
1	Phd	SPIT	Pursuing	NA
2	ME Electronics	SPCE	2005	DISTINCTION
3	BE Electronics	KJSCE	1998	FIRST CLASS

- HSC ,Mumbai Board 85.83%
- SSC ,MUMBAI Board 88.42%

- **Teaching/Industry experience in totality : 20 years**

- 1) Lecturer in Sardar Patel collage of Engineering unaided wing) from 1/1/1999 on Adhoc basis till 6/8/2000.
- 2) Sardar Patel Institute of Technology since 7/8/2000 (Before 2005 known as Sardar Patel collage of Engineering unaided wing)

▪ **Achievements:**

▪ **Awards:**

▪ **Domain expertise and areas of interest:**

▪ **Details of subjects taught:**

S.No.	Name of subject	Year and branch
•	Digital Circuits,Logic Circuits	20,All
•	ENAS,Circuit Theory	20,All
•	VLSI Design	6,ETRX
•	Image Processing	12,ETRX
•	PCS	10,ETRX
•	EDC,AE	12,ETRX
•	BEE	20,All

▪ **Workshops/Refresher courses/Training programs/Seminars attended:**

- DTE approved Winter school on “Object Oriented Programming in C++ and Java” in Sardar Patel college of Engineering in December 2000.
- Two Weeks ISTE-STTP on “DSP and its Applications” organized by Department of Electrical and Electronics Engineering, Sardar Patel College of Engg., Andheri(W),Mumbai-58, during 9th May to 20th May 2005.
- Two Days Workshop on “Computer Hardware” organized by Department of Electronics Engineering, Andheri(W),Mumbai-58, during 17th Nov & 18th Nov 2003.
- Two Weeks ISTE-STTP on “Software Tools in Electronics and Telecommunication Engineering” organized by Department of Electronics and Telecommunication Engineering, Rajiv Gandhi College of Engg, Andheri, Mumbai, during 15th Dec to 19th Dec 2008.
- One Day workshop on “Circuit Simulation” organized by Department of Electronics Engineering, Sardar Patel Institute of Technology, Andheri (W), Mumbai-58, on 28th Aug 2008.
- Two Days workshop on “DSP Design Flow” organized by CorelEL Technologies & Department of Electronics Engineering, Sardar Patel Institute of Technology, Andheri (W), Mumbai-58, on 6th & 7th Jan 2011.

- Two Days Workshop on “Technical Paper Writing” conducted by Department of Electronics Engineering, Sardar Patel Institute of Technology, Andheri (W), Mumbai-58, on 1st-2nd Sep 2011.
- CMOS, Mixed Signal and Radio Frequency VLSI Design, STTP (atleast 1 week duration)
- AICTE sponsored 2 days National Seminar on ”Technologies for Development of Rural Areas ”
- ASPIRE 2 days Workshop:
Steps to be taken in the journey of becoming a world Class institution Leadership from below,
Group exercise
- STTP AICTE-ISTE Induction/Refresher Programme on “Innovative Teaching Learning Practices to Achieve Outcome Based Education and Accreditation”,
- **Workshops/Refresher courses/Training programs/Seminars conducted as a resource person:**
- **Membership of professional bodies: ISTE**
- **Publications:**

International Conferences

Sr. No	Paper Title	Conference id	International Refereed Conference Details	Date of Publication
1	Voltage-Mode and Current-Mode FSK Demodulator using IC AD844 at circuit level	ICECEIC19	Voltage-Mode and Current-Mode FSK Demodulator using IC AD844 at circuit level IEEE SPONSORED 2019 5TH INTERNATIONAL CONFERENCE FOR CONVERGENCE IN TECHNOLOGY, PUNE (MS),INDIA, 29th -31st MARCH 2019	30th and 31st January 2019
2	Low Power Current-mode Frequency Shift Keying (FSK) Transmitter	ICCCT	International Conference on Computing and Communications Technologies (ICCCT), , India.	Sep. 2015

			ISBN:978-1-4503-3552-2. DOI:	
3	“Current Mode Full Wave Rectifier Topology for Integration	ISBN : 978-1-5386-2440-1/18/	“3rd IEEE International Conference on Recent Trends in Electronics, Information Communication Technology , 2018 .	
4	Design of low voltage supply Current Feedback Operational Amplifier	2018 .ISBN : 978-1-5386-1974-2	IEEE Proceedings of the 2nd International Conference on Inventive Communication and Computational Technologies,	2018
5	Modified Set Partitioning in Hierarchical Tree	ICSCIS-2008	4th International Conference on Soft Computing and Intelligent Systems and 9th International Symposium on Advanced Intelligent Systems (SCIS & ISIS 2008), Nagoya University, Nagoya, Japan.	18,19,20th Sept.2008
6	Comparative Analysis of Transimpedance Amplifier of 45 nm and 180nm CMOS Technology	International Journal of Scientific Engineering and Technology	Volume No.2, Issue No.10, pp : 1022-1025 (ISSN : 2277-1581)	1 Oct. 2013
7	Current Mode	INTERNATIO	IJIREICE, Volume 3,	2015

	based Communication System	NAL J OURNAL OF I NNOVATIVE R ESEARCH IN E LECTRICAL, E LECTRONICS, I NSTRUMENT ATION AND C ONTROL E NGINEERING	Issue 3, March 2015 DOI 10.17148/IJIREEICE.2015 .3301	
Sr. No	Paper Title		National Conference Details	Date of Publication
8	Human Visual System Based Medical Image Compression Using Set Partitioning In Hierarchical Trees (SPIHT)		National Conference on Biomedical Engineering (NCBME-2006) At Sardar Patel College of Engineering, Andheri(w)	28,29 March 2006
9	Set Partitioning In Hierarchical Trees (SPIHT) Image Coding Algorithm		At D.J.Sanghvi College of Engineering, Vile Parle (w)	2006
10	Energy Efficient Transimpedance Amplifier Akshata Raut Narendra Bhagat		2013 International Conference on Machine Intelligence Research and Advancement	3013

Nature of program	Organizing institute/body	Topic/theme	Duration	Grade awarded, if any	
Conference/ seminar	International conference on Electrical, Communication, Electronics, Instrumentation and	FSK receiver using Current Conveyors	2 day s	NA	

(06)

	computing(ICE CEIC-2019),IEEE Madras section, Tamilnadu ,India				
	5 th International Conference for Convergence in Technology 2019 ,supported by IEE Bombay Section(I2CT)	Voltage-Mode and Current-Mode FSK Demodulator using IC AD844 at circuit level			

Nature of program	Organizing institute/body	Topic/theme	Duration	Grade awarded, if any	
Conference/ seminar	Coimbatore, Karpagam Collage of Engineering	Intelligent Computing and Control(I2C2)	2 days		(06)
Workshop					(02)
STTP (atleast 1 week duration)	IIT KHARA GPUR	CMOS, Mixed Signal and Radio Frequency VLSI Design, AICTE sponsored 2 days National Seminar on "Technologies for Development of Rural Areas" ,	2 weeks	Very Good	

▪ **Details of National and International Journal publications:**

S.No	Title of Journal	Title of paper	Co-authors
1.	<i>IOSR Journal of VLSI and Signal Processing (IOSR-JVSP) Volume 9, Issue 3, Ser. I (May. - June. 2019), PP 01-10 e-ISSN: 2319 – 4200, p-ISSN No. : 2319 – 4197</i>	“ Current Mode Current Injection Technique for Gain and Bandwidth Enhancement for Novel CFOA “	Uday Pandit Khot
2.	International Journal of innovative research in electrical , electronics, instrumentation and control engineering, vol. 3, no. 2, March -2015.DOI: 10.17148	Current Mode Based Communication System	Uday Pandit Khot
3.			
4.			

▪ **Details of Book Chapters Published:**

▪ **R& D and Consultancy:**

▪ **Contribution to Corporate life and management of the Department and Institution:**

Administration activities at Sardar Patel Institute Of Technology. Andheri (w)

- Executive Committee member for the National Conference on Biomedical Engineering. Conducted in 2006.
- Associated with committee for accreditation work for NBA
- Vice President of The Gymkhana Since August 2005.
- In charge Of the Cultural festival “ SPACE” For Years 2005 to 2015 .

- Assisted in conducting various seminars and short term courses

▪ **Other Contributions:**

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Nature of role, e.g. resource person, consultant, guest lecturer	Name of institute or industry	Description of activity/contribution	Duration
Conducted Interview	MPSTME	Interviewed 12 candidates for the post of Assistant Professor(4 posts) in Mechatronics Departement on !3/02/2019	
Paper setter and Auditor at MPSTME and SPCE			

- Organization : Bhavans College of Information technology
Course : B.Sc. (IT)
Designation : Visiting Faculty for Digital Design course
- Organization : Mukesh Patel Institute Of Technology, Vile Parle(west)
Course : B.Tech, M.Tech
Designation : Visiting Faculty for Electronics, Digital Logic Design, Advanced Microwave Engineering