

PROF. NAJIB GHATTE

PERSONAL PROFILE

Father's Name: ADV. NISAR GHATTE

Date of Birth: 23rd October, 1989

Age: 30 years

Gender: Male

Marital Status: Married

CONTACT INFO

Flat No. 1002, Al-Aziz Palace,
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OBJECTIVE

To facilitate student learning in the field of digital and analog electronics, control system, Embedded Systems with research background, that will offer me the best opportunity for further development of my abilities, skills and knowledge in an established firm with long term career growth possibilities.

EDUCATIONAL QUALIFICATION

| Sr. No. | Institution | University/ Board | Year of Passing | Course | Percentage |
|---------|--|---|-----------------|--------------------------|------------|
| 01 | Fr. Conceicao Rodrigues College of Engineering, Mumbai | University of Mumbai | 2015 | M. E. (Electronics) | CGPI: 8.96 |
| 02 | M. H. Saboo Siddik College of Engineering, Mumbai | University of Mumbai | 2011 | B. E. (Electronics) | 71.29% |
| 03 | Wilson College, Mumbai | Maharashtra State Board of Secondary and Higher Secondary Education | 2007 | 12 th Science | 82.67% |
| 04 | St. Joseph's High School, Mumbai | Maharashtra State Board of Secondary and Higher Secondary Education | 2005 | 10 th | 78.53% |

WORK EXPERIENCE

| Sr. No. | Name and Address of Employer | From | To | Designation/ Nature of Work |
|---------|--|----------------------------|---------------------------|--|
| 1 | Bhartiya Vidya Bhavan's Sardar Patel Institute of Technology | 1st July 2016 | Present | Assistant Professor, Electronics Department |
| 2 | VJTI Wadala, Mumbai | 1 st July 2015 | 31 st May 2016 | Assistant Professor, EXTC Department |
| 3 | M. H. Saboo Siddik Polytechnic, Mumbai | 20 th June 2013 | 15 th May 2015 | Lecturer in Electronics & Telecommunication Engineering, Addl. QC Supervisor for RAC-Winter-'13 and Summer-'14 under MSBTE, Cultural In-charge |

SOFTWARE DEXTERITY

Languages:

C, C++, HTML, VHDL, Verilog, Microcontroller Assembly, Embedded C.

Software known:

Eagle, Flash Magic, Multisim, Circuit Wizard, Kiel μ Vision, Proteus VSM, MATLAB
FDA Tools, Basic MATLAB Computing, Xilinx, Questasim, Spice, Circuit Maker, Arduino.

PROJECTS UNDERTAKEN

A. Research Projects:

Universal MODBUS Data Logger

Medicines and Drugs Physical Parametric Data is logged and stored in server in real time format.

Received Grants from Mumbai University Rs. 30,000/-
In year 2018-2019

Ref No. APD/237/601 of 2019 dated 27th Mar 2019
of Research Project No. R-170

Solar Tracking System

Solar energy is tracked as per MPPT and then further used to charge battery for inverter to drive home appliances.

A. Master of Electronics Engineering

Final year Project:

High Speed Floating point Arithmetic Engine using Vedic Sutras

Vedic Sutras are employed to develop an efficient ALU to have pace with the advance computers.

B. Bachelor of Engineering Projects

Final Year Project:

Design of Vessels Bilge Oil Water Separator

Tracks the water and oil containment in the bilge tank and displays level on screen and sounds buzzer if it crosses the threshold.

Term Projects:

- Design and Implementation of Door Guard (Sem-IV)
- Design and Implementation of Microcontroller based Electronic Voting Machine (Sem-IV)
- Design and Implementation of Heart Rate Detector (Sem-V)
- Design of 8-bit UART using VHDL (Sem-V)
- Design and Implementation of Microcontroller based Logic Gate Emulator (Sem V)
- Design of Adders and Multipliers using VHDL (Sem-VII)
- Many other Mini (Analog and Digital) Projects as a mentor for Sem-IV Electronics Engineering Degree Students

Presentation and Case Studies:

- Transmission Electron Microscopy
- Handwritten Character Recognition
- DC Power Supply Design

Publication:

- Numeric Water Level Indicator (EFY Publication)
- Tachometer (EFY Publication)
- Adders and Multipliers (upload on wesrch/docstoc)
- Network Protocol (upload on wesrch/docstoc)
- IEEE format Guidelines (upload on Scribd)

C. Master of Engineering
First Year:

- Modelling Light Emitting Diode using SPICE (published in IJRAT vol 1 Issue 5, December 2013)
- Cyclic Redundancy Check Simulation using Serial Communication (published in IJAREEIE vol 3 Issue 1, January 2014)
- Real-Time Clock Implementation using I²C Protocol Check (published in IJAREEIE vol 3 Issue 1, January 2014)

Second Year:

- Speed ALU Design using Vedic Sutras (published in IJETT V8(4), February 2014)
- Floating Point Engine using VHDL (published in Pragyana-14 ICRTCEE)
- Double Precision Floating point Square Root Computation (Published IJETT V13(6), Jul. 2014)
- Single Precision Floating point Square Root Computation (Presented ICSTACE, Aug. 2014)
- Single Precision Floating point Division (Presented ICSTACE, Aug. 2014)

ACHIEVEMENTS:

School Level:

Proficiency

Gungavrav Patra in Std V for Securing 80%

Gungaurav Patra in Std VI for Securing 76.67%

| Sr. No. | Subject/Event | Std. | Position/Remarks |
|---------|---|------|------------------|
| 01 | E. V. S. | II | Highest marks |
| 02 | Science | III | Highest marks |
| 03 | English Grammar and Composition | III | Highest marks |
| 04 | Geography | III | Highest marks |
| 05 | English | V | Highest marks |
| 06 | Social Studies | V | Highest marks |
| 07 | Hindi | VIII | Highest marks |
| 08 | Drawing Competition | VII | Consolation |
| 09 | Drawing Competition | VIII | Consolation |
| 10 | Bournvita Quiz Contest | VIII | Participation |
| 11 | 'SPARSH' Rajiv Gandhi Computer Competition | IX | Participation |
| 12 | Card Competition | V | Second |
| 13 | <i>Brihan Mumbai Nimmastar Marathi Shikshak Sanghatna</i> | IX | Second |
| 14 | <i>Brihan Mumbai Nimmastar Marathi Shikshak Sanghatna</i> | X | First |
| 15 | Datatech Computer Academy | X | 'A' Grade |
| 16 | Character Certificate | X | Good |

Junior College Level: Have won 2nd Prize in *Kavita-Path Pratiyogita*

Engineering Level:

| Sr. No. | College | Festival | Event | Year | Rank |
|---------|---|---------------------|--------------------------|------|--------|
| 01 | M. H. Saboo Siddik College of Engineering, Byculla | Automotivation 2009 | Robo Soccer | 2009 | First |
| 02 | M. H. Saboo Siddik College of Engineering, Byculla | Automotivation 2009 | Robotics-Obstacle Course | 2009 | Second |
| 03 | K. J. Somaiya Institute of Engineering and Information Technology, Sion | RENAISSANCE'09 | Fifabotics | 2009 | Second |
| 04 | Thadomal Shahani Engineering College, Bandra | ISAAC 2009 | Robotics-Own Goal | 2009 | Second |
| 05 | Pillai's Institute of Information Technology, New Panvel | IMPULSE 2010 | Robo Soccer | 2010 | Second |
| 06 | Thadomal Shahani Engineering College, Bandra | ISAAC 2010 | Robo Kickstart | 2010 | Second |
| 07 | Fr. Conceicao Rodrigues College of Engineering, Bandra | Crescendo-2010 | Robo Soccer | 2010 | First |
| 08 | M. H. Saboo Siddik College of Engineering, Byculla | Purple 2010 | Robo Soccer | 2010 | First |
| 09 | M. H. Saboo Siddik College of Engineering, Byculla | Purple 2010 | Robo Wars | 2010 | First |
| 10 | Rizvi College of Engineering, Bandra | Rays 2010 | Robo Wars | 2010 | First |

Post Engineering Level:

- Have elected as Joint Technical Head of M. H. Saboo Siddik College of Engineering under IEEE chapter for the 2009-2010
- IEEE member for the consecutive two years: 2009-2011
- SACE member in the year :2009-2010
- Green Day Volunteer at M. H. Saboo Siddik College of Engineering in 2009-2010
- Secured Highest Marks in ECAD-II (Sem-IV) and Embedded Systems (Sem-VIII)
- Presented 'Vessels Bilge Oil Water Separator' at Inter- College Technical Paper Meet-2012 organized by Saraswati College of Engineering on 14th Sept. 2012
- Participated in Two-Day Workshop on System Modelling and Simulation organized by Fr. Conceicao Rodrigues College of Engineering
- Participated in International Conference on Advance Computing, Communication and Control organized by Fr. Conceicao Rodrigues College of Engineering

Workshops Attended:

- Attended 2016 International Symposium Internet of **Things** organized by Lab-X Foundation in collaboration with Sardar Patel Institute of Technology and IEEE Consumer Electronics Society held in Mumbai, on August 3rd and 4th 2016
- Attended Design **Thinking Workshop** conducted by SPJIMR faculties at Sardar Patel Institute of Technology on January 3rd and 4th 2017 and presented a report on Empathy Map for stakeholders on 18th April 2017.
- Attended talk on **Research Methodologies** by Dr. S. Y Mahaska Pro VC of Mukesh Patel on 14th Nov. 2017
- Attended AICTE Sponsored **Two Days National Seminar** on Technologies for Development of Rural Areas" on 27th and 28th Nov 2017.
- Attended ISTE approved **One Week STTP** on Pushing Boundaries for Wireless Technologies" from 2nd Jan to 6th Jan 2018
- **Delivered an Expert Lecture** on "Gate Orientation" in Electrical Engineering Department of, New Panvel on Monday, 12th February 2018
- Attended **Faculty Development Program** on 11th July 2018 on Machine Learning for Data Analytics and Big Data Applications using MATLAB, Hardware Connectivity and Internet of Things using MATLAB and Simulink

STRENGTHS:

Sincere, honest, hardworking person and Quick learner with right attitude, self-confidence and pleasing nature.

DECLARATION

I hereby declare that whatever furnished above is true and correct to the best of my knowledge and belief.

Date: 24th September 2019

Place: Mumbai

(Prof. Najib Ghatte)

Publication References

International Journal Publications (05)

- [1] Dattaprasad Madur, **Najib Ghatte**, Vinit Pereira, Tushar Surwadkar, “Modelling Light Emitting Diode using SPICE” in *International Journal of Research in Advent Technology*, V1(5), pp.366-371, Dec. 2013.
- [2] Vinit Pereira, **Najib Ghatte**, Madur Dattaprasad, Tushar Surwadkar, “Cyclic Redundancy Check Simulation using Serial Communication” in *International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering*, V3(1), pp.6505-6510, Jan. 2014.
- [3] **Najib Ghatte**, Vinit Pereira, Madur Dattaprasad, Tushar Surwadkar, “Real-Time Clock Implementation Using I²C Protocol Check” in *International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering*, V3(1), pp.6511-6516, Jan. 2014.
- [4] **Najib Ghatte**, Shilpa Patil, Deepak Choir, "Floating Point Engine using VHDL " in *International Journal of Engineering Trends and Technology(IJETT)*, V8(4), pp. 198- 203 Feb. 2014.
- [5] **Najib Ghatte**, Shilpa Patil, Deepak Bhoir, "Double Precision Floating Point Square Root Computation" in *International Journal of Engineering Trends and Technology(IJETT)*, V13(6), pp. 294-298 Jul. 2014.

International Conference Publication (05)

- [1] **Najib Ghatte**, Shilpa Patil, Deepak Bhoir, “High Speed ALU Design using Vedic Sutras” in *Pragyanam '14 Proc. Int. Conference on Recent Trends in Computer and Electronics Engineering.*, Mumbai, 2014 pp. 227-232.
- [2] **Najib Ghatte**, Shilpa Patil, Deepak Bhoir, " Single Precision Floating Point Square Root Computation " in *Fifth IRF Proc. International Conference on Software Technology and Computer Engineering*, Goa, pp. 29-33, Aug. 2014.
- [3] **Najib Ghatte**, Shilpa Patil, Deepak Bhoir, " Single Precision Floating Point Division " in *Fifth IRF Proc. International Conference on Software Technology and Computer Engineering*, Goa, pp. 34-38, Aug. 2014.
- [4] Saqib G, Faizan K, **Ghatte N**, “Intelligent Chatting Service Using AIML”, *2018 IEEE International Conference on Current Trends towards Converging Technologies (ICCTCT 2018)*, 1st – 3rd March 2018, SVS College of Engineering, Coimbatore, Tamil Nadu, India
- [5] Deepak Karia, Ankur Mahadik, Suma Salian, Vaibhav Rajput, Anuradha Jadiya and **Najib Ghatte**, “Fire Detection and Gas Leakage Prevention System”, *ICNTET2018: International Conference on New Trends in Engineering & Technology*, September 7-8, 2018, Chennai, India,