



Payal Hitesh Shah

Assistant Professor, Electronics Department,
Sardar Patel Institute of Technology

Contact: 9867368965

Email id: payal_shah@spit.ac.in

Mobile: 9867368965

- **Date of joining the present institute: 19/07/2006**
- **Date of joining the present department: 19/07/2006**
- **Scholastic record: (*Begin with the highest degree and end with XII*)**

S. No	Degree	Institute	Year of passing	Class/CGPI/percentage
1	Masters of Engineering (M.E.) <i>Automatic Control and Robotics</i>	The Maharaja Sayajirao University of Baroda, Vadodara	2005	70.71 (First Class with Distinction)
2	Bachelor of Engineering (B.E.) <i>Electrical Engineering</i>	The M.S. University of Baroda, Vadodara	2003	62.30 (First Class)
3	H.S.C.	Gujarat State Education Board, Gandhi Nagar	1999	81.85 (Distinction)

- **Teaching/Industry experience in totality :**

- 1) Working as an assistant professor in Electronics Department of Sardar Patel Institute of Technology, Mumbai since July 2006
- 2) Worked as a teaching assistant in Electrical and Electronics Engineering Department of Faculty of Technology and Engineering of The MS university of Baroda from July 2005 to January 2006

- **Domain expertise and areas of interest:** Electronics devices and circuits, Artificial Intelligence, Neuromorphic engineering

- **Details of subjects taught:**

S.No.	Name of subject	Year and branch
1.	CMOS VLSI Design	4 th -ETRX
2.	Basics of Electronics & Electrical Engg.	1 st -ETRX, IT, COMPUTER
3.	Discrete Electronics Circuits	2 nd - ETRX
4.	Electronic Measuring Instruments	2 nd - ETRX
5.	Continuous Time Signals & Systems	3 rd -ETRX
6.	Electrical Networks	2 nd - ETRX
7.	Electronics Circuits Analysis & Design	2 nd - ETRX
8.	Electronics & Electrical Measuring Instruments & Machines	2 nd - ETRX
9.	Communication Systems	3 rd -EXTC
10.	Microwave and Fiber Optic Communication	3 rd -EXTC
11.	DLDA	2 nd -IT

12.	Electrical Networks Analysis & Synthesis	2 nd -COMPUTER
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Workshops/Refresher courses/Training programs/Seminars attended and conducted as a resource person:

Course / Training Name(participant/organizing committee member)	Institute / Firm	Duration
NPTEL-AICTE Faculty development programme on “ Soft Skills” (participant)	IIT Roorkee	July-Oct. 2019 (1.5 Weeks)
AICTE Sponsored Faculty development programme on “Consumer Electronic Product Design, Testing, Reliability and Patenting” (organizing committee member)	Sardar Patel Institute of Technology, Mumbai	July 2019 (2 Weeks)
AICTE Sponsored STTP on “Front End VLSI Design and Verification” (organizing committee member, participant)	Sardar Patel Institute of Technology, Mumbai	June 2019 (1 Week)
NPTEL-AICTE Faculty development programme on “ CMOS Digital VLSI Design” (participant)	IIT Roorkee	Feb-April 2019 (1week)
NPTEL-AICTE Faculty development programme on “ Teaching And Learning in Engineering(TALE)” (participant)	IISc Bangalore	Feb-March 2019 (1/2 week)
Workshop on “Flipped Learning (participant)	Bhavan’s College, Mumbai	Oct. 2018(2 days)
AICTE-ISTE sponsored Faculty development programme on “Innovative Teaching Learning Practices to Achieve Outcome Based Education and Accreditation”(organizing committee member, participant)	Sardar Patel Institute of Technology, Mumbai	May 2018 (1 week)

AICTE-ISTE approved CAS programme on “ Numerical Techniques in Engineering Research” (participant)	Sardar Patel Institute of Technology, Mumbai	Dec 2017 (1 week)
AICTE Sponsored National Seminar on “ Technologies for Development of Rural Areas” (organizing committee member, participant)	Sardar Patel Institute of Technology, Mumbai	Nov. 2017 (2 days)
Faculty development programme on “Open Source Tools for Developing Innovative Things”(Co-ordinator, resource person)	Sardar Patel Institute of Technology, Mumbai	July 2017 (1 week)
ISTE STTP on “ CMOS, Mixed Signal and Radio frequency VLSI Design”	IIT Kharagpur, Remote Centre SPIT, Mumbai	Dec. 2016-Feb. 2017 (2 weeks)
Troubleshooting event (TSE-2016) (Organizing committee member)	Sardar Patel Institute of Technology, Mumbai	Sept. – Oct. 2016 (3 days)
Workshop on “Open Source Simulation Software” (Resource person)	SPIT IEEE & IETE	October 2016 (1 day)
Course on “ Introduction to Research”	IIT Madras (NPTEL)	July- Sept 2016 (8 weeks - 20 Hrs)
Faculty development programme on “Use of ICT in Education for Online and Blended Learning”(participant)	IIT Bombay, Remote Centre SPIT, Mumbai	May-July 2016 (4 weeks)
Faculty development programme on “Outcome Based Education-Innovative Teaching Learning Practices and Evaluation”(organizing committee member)	Sardar Patel Institute of Technology, Mumbai	May 2016 (5 days)
Faculty development programme on “Advanced Technologies in Electronics Engineering”(organizing committee member)	Sardar Patel Institute of Technology, Mumbai	Nov.-Dec. 2015(5 Weeks)
Troubleshooting event (TSE-2015) (co-ordinator)	Sardar Patel Institute of Technology, Mumbai	Sept 2015(3 days)

Faculty development programme on “Basic Electrical, Electronics and Electrical Machines”(organizing committee member, resource person)	Sardar Patel Institute of Technology, Mumbai	July 2015(1 Week)
Faculty development workshop on Embedded System Design using ARM mbed & Cypress PSoC(participant)	Sardar Patel Institute of Technology, Mumbai	May 2015(2 days)
Faculty development programme on “Electronic System Design: From Devices to Applications”(organizing committee member, participant)	Sardar Patel Institute of Technology, Mumbai	May 2015(2 Weeks)
CEP course on “Use of Circuit Simulation In Teaching Electronics”(participant)	IIT Bombay	March- 2015(2 days)
Faculty development workshop on ARM mbed PLATFORM	Sardar Patel Institute of Technology, Mumbai	July 2013(1 day)
Workshop on Analog Electronics (participant)	IIT Kharagpur, Remote Centre SPIT, Mumbai	June 2013 (2 Weeks)
Workshop on Technical Paper Writing (participant)	Sardar Patel Institute of Technology, Mumbai	July 2011 (2 days)
Workshop on Neural Network and Fuzzy Logic (co-coordinator)	Sardar Patel Institute of Technology, Mumbai	March 2010 (2 days)
Workshop on Embedded System Design with DSP Implementation (laboratory expert)	The M.S. University of Baroda, Vadodara	Dec 2005 (2 Weeks)
Workshop on Designing with Digital Signal Processors and Controllers (participant)	The M.S. University of Baroda, Vadodara	Jun 2004 (2 Weeks)
Workshop on Mixed Programming using C/C++ and MATLAB for Engineering Applications (laboratory expert)	The M.S. University of Baroda, Vadodara	Dec 2003 (2 Weeks)
Vocational Training	Jyoti Limited, Vadodara	Jun 2002 (2 Weeks)

- **Membership of professional bodies:** ISTE [Life Member (LM 91071)]
- **Publications:**

International Conferences

S.No	Publisher	Title of International conference	Title of paper/article/poster presentation	Schedule and venue of conference
1.	IEEE Bombay Section	IBSSC 2019	AgriBot - An intelligent interactive interface to assist farmers in agricultural activities	IITB, Mumbai; 26 th - 28 th July 2019,
2.	RVS Technical Campus, Coimbatore	ICECA 2019	Proportionate Study of Neuron Models for Implementation of Neural Encoder	RVS Technical Campus, Coimbatore; 12 th -14 th June , 2019
3.	SCAD College of Engineering and Technology, Tirunelveli.	ICOEI 2019	Driver Assistant System using Haar Cascade and Convolution Neural Networks(CNN)	SCAD College of Engineering and Technology, Tirunelveli; 23 rd -25 th April ,2019
4.	Vaigai College of Engineering, Madurai	ICICCS 2018	Efficient and Feasible Gesture Controlled Robotic Arm	Vaigai College of Engineering, Madurai, 14 th -15 th June 2018
5.	Karpagam College Of Engineering, Coimbatore	I2C2'17	Feature Extraction and Selection Methods for Motor Imagery EEG Signals : A Review	Karpagam College Of Engineering, Coimbatore; 23 rd - 24 th June 2017
6.	Karpagam College of Engineering, Coimbatore	ICIIECS'16	Range Doppler Algorithm for Ground Based FMCW X-band Synthetic Aperture Radar	Karpagam College of Engineering, Coimbatore; 17 March 2016
7.	KCCEMSR, Mumbai	ICONECT'16	Atmosphere tracking system using AVR controller	KCCEMSR, Mumbai; 10 th -11 th March 2016
8.	DBIT, Mumbai	ICTSD 2015	A low powered Auto-titrator using pH endpoint detection	DBIT, Mumbai; 4 th – 6 th Feb. 2015
9.	VJTI, Mumbai	INTERFACE 2014	Vehicular data acquisition system	VJTI, Mumbai; 27 th -28 th June 2014

▪ **Details of National and International Journal publications:**

S.No	Title of Journal	Title of paper	Co-authors
1.	IOSR Journal of VLSI and Signal Processing	A Hybrid Approach for MRI Based Statistical Feature Extraction to Detect Brain Tumor	Narbada Jhalwa, Dr. Rajendra Sutar

▪ **Contribution to Corporate life and management of the Department and Institution:**

Name of institute/ Government body	Role/Position/ Name of committee	Description of service/contribution
Sardar Patel Institute of Technology	CEP Co-ordinator	CEP Co-ordinator for ETRX department.
	Committee member of Syllabus setting team for autonomy	Helped in setting and revising syllabus for various courses of ETRX dept. like BEE ,EMIL, EM, AE- I, AE- II, CVD, Instrumentation
Mumbai University	Examiner	Correction, revaluation, moderation of university exam papers
Any other	Member of Department library committee	Look after the issue mechanism of books
	Member of NBA committee	Updating of files regarding SFR calculation, meeting file, department library, guest lecture