



Prof. Priya Deshpande

Assistant Professor, Electronics Department,
Sardar Patel Institute of Technology

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- **Date of joining the present institute:** 1st August 2013
- **Date of joining the present department:** 1st August 2013
- **Scholastic record:** (*Begin with the highest degree and end with XII*)

S.No	Degree	Institute	Year of passing	Class/CGPI/percentage
1	Ph.D (Pursuing)	Indian Institute of Technology Bombay	-	Ph.D Course work (CPI: 8.23)
2	M.Tech (EXTC)	Veermata Jijabai Technological Institute, Mumbai	2012	GPA: 9.9 (1 st Rank)
3	B.E (EXTC)	Sant Gadge Baba Amravati University	2010	70.82%

- **Teaching experience in totality : 06 Years**
- **Academic merit record and achievements:** (*only in graduation and post-graduation years*)
 1. Got the Gold Medal for Securing 1st position in MTech of all departments in VJTI in 2012.
- **Domain expertise and areas of interest:**

Interested in Image Processing, Microprocessor and Microcontrollers and MATLAB programming

▪ **Details of subjects taught:**

S.No.	Name of subject	Year and branch
1.	Micro-architectures	2019 (ETRX)
2.	Computer Organization and Architecture	2018 (ETRX)
3.	Micrcontroller and applications	2016(ETRX), 2017(ETRX), 2018(ETRX)
4.	Microprocessor and Peripherals	2014(ETRX), 2015(ETRX)
5.	Circuit Theory	2013(ETRX),2014(ETRX), 2015(ETRX), 2016(ETRX)
6.	Microprocessor and Peripherals	2015(ETRX), 2017(ETRX)
7.	Continuous Time Signal System	2013(ETRX),
8.	Microwave Devices and Circuits	2014(ETRX),
9.	Digital Image Processing and Design	2014(ETRX),

▪ **Workshops/Refresher courses/Training programs/Seminars attended:**

1. AICTE Sponsored Two Week FDP on “Consumer Electronics Product Design, Testing, Reliability and patenting” in Sardar Patel Institute of Technology, Andheri from 1st July to 13th July 2019
2. Completed one month Summer Internship in Sardar Patel Institute of Technology on “Embedded Systems and IOT” from May 28th to June 28th 2018 with Grade of 9.0.
3. AICTE-ISTE Induction/Refresher Programme on “Innovative Teaching Learning Practices to Achieve Outcome Based Education and Accreditation” (Responses) on 3rd may to 9th may 2018 in Sardar Patel Institute of Technology, Andheri
4. Two week STTP on “CMOS, Mixed Signal and Radio Frequency VLSI Design” conducted by IIT Kharagpur from 26th Dec to 4th Feb 2017.

5. Got a “ELITE” Certificate with score of 80% for NPTEL Online Certification course for the subject of Digital Image Processing in Jul to Oct 2016 (30 hrs Course)
6. Two days International Symposium on Internet of Things on 3rd and 4th august 2016 in Sardar Patel Institute of Technology, Andheri.
7. ISTE Approved One Week STTP on “Internet of Things” organized by FCRCE College, Bandra from 27th June to 1st July 2016.
8. Two day FDP on “Embedded System Design using ARM mbed & Cypress PSoC.
9. AICTE Approved Two week FDP on “Electronic System Design: From Devices to Applications” during 4th to 15th May 2015 in Sardar Patel Institute of Technology, Andheri.
10. TEQIP - II Sponsored two week STTP on “Signal & Image Processing with Wavelets” from 16th to 25th Dec 2013 at VJTI, Mumbai.
11. AICTE (Under QIP) Sponsored one week FDP on “Signal Processing and Applications” from 23rd to 27th Feb 2015 at VJTI, Mumbai.

- **Membership of professional bodies:**

- **ISTE Membership**

- **Publications:**

International Conferences

S.No	Publisher	Title of International conference	Title of paper/article/poster presentation	Schedule and venue of conference
1.	IEEE	International Conference on Communication Information and Computing Technology (ICCICT)	HYDROBOT: An Underwater surveillance robot	2-3 Feb 2018, Sardar Patel Institute of Technology, Andheri

- **Details of National and International Journal publications:**

S.No	Title of Journal	Title of paper	Co-authors
1.	International Journal of Computer Applications, 2012	Electrical Impedance Tomography using EIDORS in a Closed Phantom	Vidya Sarode, Priya M. Chimurkar, Alice N. Cheeran,
2.	International Journal of Engineering Sciences & Emerging Technologies, 2012	Electrical Impedance Tomography (EIT) based Medical Imaging using Finite Element Method (FEM)	Vidya Sarode, Priya M. Chimurkar, Alice N. Cheeran

▪ **Contribution to and role played in the administrative affairs of the institute:**

1. Appointed as Project Co-ordinator (Electronics Department) from 2018 -2019
2. Working as a Faculty Co-ordinator for ABL-2 activity based on Legal Studies, 2017
3. Worked as a Faculty Co-ordinator for ABL-1 activity based on Fire Safety and successfully done the activities under this event with FSAI (skit comp, poster and short film), 2017 and 2018
4. Worked as a Faculty Co-ordinator for ABL-3 activity based on Creative Thinking,Diversity and Workplace Etiquettes in 2018 and 2019
5. Worked as a Event Co-ordinator for Troubleshooting Event (TSE-2017)
6. Worked as Event Co-ordinator for MiniProject-I Competition held on 7th Nov 2017
7. Worked as Event Co-ordinator for MiniProject-II Competition on 24th April 2017