

Dr. RAJENDRA G. SUTAR

Dean Academics,

Sardar Patel Institute of Technology,
Andheri (W), Mumbai

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SCHOLASTIC RECORD

Sr. No.	Degree	Institute	Year of Passing	Percentage
1	PhD (ECE)	V.N.I.T., Nagpur	2014	--
2	ME (ETRX)	S. P.C.E. (Unaided), Mumbai	2008	68.10

Thesis Title: “Design of Intelligent Diagnostic Support System for Cardiovascular Disease (CVD), using Effective Signal Processing Techniques”.

WORK HISTORY (TOTAL EXPERIRNCE: 20+ YEARS)

01.09.2019-Present	SPIT, Mumbai. Dean Academics, Nature of duties: Academic Administration of the Institute.
01.01.2017-31.08.2019	SPIT, Mumbai. Deputy HOD, Electronics Engineering Department Nature of duties: Administration of the Department
(2+ YEARS) 05.2016 - PRESENT	SPIT, Mumbai. Associate Professor Nature of duties: Teaching UG/PG Students
(10+ YEARS) 01.2006 - 05.2016	SPIT, Mumbai. Assistant Professor Nature of duties: Teaching UG Students
(04 YEARS) 01.2002 - 12.2005	SPCE (UA), Mumbai. Lecturer in Electronics Department Nature of duties: Teaching UG Students
(03 YEARS) 08.1998 – 12.2001	BHARATI VIDYAPEETH’S INST. OF TECHNOLOGY, NAVI MUMBAI Lecturer in Electronics & Telecommunication Department Nature of duties: Teaching Undergraduate Students
(01 YEAR) 09.1997 – 08.1998	CONCEPT INTEGRATIONS PRIVATE LIMITED, PUNE System Engineer Nature of duties: Production, Quality Assurance, Quality Control, Research and Development of Biomedical Instruments.

Experience as PG Teacher

1. Approved P.G.-Teacher for Electronics & Telecommunication Course.
M. E.- (Mumbai University Approval No. **PG/ICD/2015-16/556** Dated 23/06/2015)
Ph.D.- (Mumbai University Approval No. **PG/2/ICD/2017-18/4633** Dated 29/09/2017)
2. Guided 05 PG Students.

Publications:

1. R. G. Sutar, and A. G. Kothari, "Intelligent electrocardiogram pattern classification and recognition using low-cost cardio-care system," *IET Science, Measurement & Technology*, vol. 9, no. 1, pp. 134-143, 2015. (SCI Journal)
2. R. G. Sutar, and A. G. Kothari, "An Algorithm for Detection of ECG Morphological Points Using its Non-Linear Energy," *Inderscience, International Journal of Biomedical Engineering and Technology (IJBET)*. (SCOPUS Indexed)
3. Amol Golwankar, Pranav Pailkar, Purvika Patil and Rajendra Sutar, "Optimization of Data Set For Classification Of Diabetic Retinopathy Using Support Vector Machine With Minimal Processing," *Inderscience, International Journal of Biomedical Engineering and Technology (IJBET)*. (SCOPUS Indexed)
4. Narbada Jhalwa, Payal Shah, and Rajendra Sutar, "A Hybrid Approach for MRI Based Statistical Feature Extraction to Detect Brain Tumor, " *IOSR Journal of VLSI and Signal Processing (IOSR-JVSP)*, vol. 8, no. 2, pp. 30-37, 2018.
5. Diksha D. Halave and Rajendra G. Sutar, "Signal Processing Techniques For Classification Of Fetal Electrocardiogram: A Survey," *IOSR Journal of VLSI and Signal Processing (IOSR-JVSP)*, vol. 9, no. 2, pp. 34-33, 2019.
6. R. G. Sutar, and A. G. Kothari, "A Low-Cost Diagnostic Support System for Remote Cardiac Healthcare" *6th IEEE RAS/EMBS International Conference on Biomedical Robotics and Biomechatronics*, June 26-29, 2016. NUS, Singapore. (SCOPUS Indexed)
7. R. G. Sutar, A. G. Kothari, and A. G. Keskar, "Development of an embedded system for real time Heart Rate Variability analysis," in *Proc. IEEE International Symposium on Communications and Information Technologies (ISCIT)*, 4-6 Sept. 2013, **Surat Thani, Thailand**.
8. R. G. Sutar, and A. G. Kothari, "Detection of cardiac hypertrophy by ECG analysis," in *Proc. IEEE International Conference on Communication, Information & Computing Technology (ICCICT)*, 19-20 Oct. 2012, **Mumbai, India**.
9. R. G. Sutar, A. G. Kothari, and A. G. Keskar, "ECG Feature Extraction Using LCAD," in *Proc. IEEE international Conference on Communication Systems and Network Technologies (CSNT)*, 11-13 May 2012, **Rajkot, India**.
10. Varad Choudhari, Vaishnav Dandge, Natasha Choudhary, and **Rajendra G Sutar**, " A portable and low-cost 12-lead ECG device for sustainable remote healthcare," in *Proc. IEEE international Conference on Communication information and Computing Technology (ICCICT-18)*, 2-3 Feb. 2018, **Mumbai, India**.

11. Pooja D Sonawane and **Rajendra G Sutar**, “A schematic review on body area networks for E-health systems,” in *Proc. IEEE international Conference on International Conference on Intelligent Computing and Control (I2C2)*, 23-24 June 2017, **Coimbatore, India.**
12. Rijuta V Wankar, Payal Shah, and **Rajendra G. Sutar**, “Feature extraction and selection methods for motor imagery EEG signals: A review,” in *Proc. IEEE international Conference on International Conference on Intelligent Computing and Control (I2C2)*, 23-24 June 2017, **Coimbatore, India.**
13. P. V. Kasambe, R. G. Sutar, and D. D. Ambawade, “Real Time Heart Rate Variability Analysis using Virtual Instrumentation,” in *Proc. International Conference on Communication Computers & Instrumentation (ICCCI)*, Jan. 2009, **Mumbai India.**

R& D and Consultancy:

1. Fetched **minor research grant** of Rs.30149/- for the project titled, ‘**Multi-parameter Diagnostic Support System for Connected Health, at Primary Health Centers of Rural Area**’ in 2014-15. This project is for the better healthcare in the rural area. Three parameters namely, ECG, NIBP and Pulse Oxygen have been used for the design of the system.
2. Worked as **Invited Reviewer** for an SCI Journal: **IET Science, Measurement and Technology.**
3. Worked as **Invited Reviewer** for IEEE, Springer Conferences.

Contribution to Corporate life and management of the Department and Institution:

- 1) Working as Dean Academics (S.P.I.T.) since September 2019
- 2) Worked as Dy. H.O.D. of Electronics Department (January 2017-August 2019)
- 3) Worked as member of centralized OMS admission Committee in SPCE (Unaided) during 2003-04 and first year admission Committee in SPIT during 2004-2008.
- 4) Worked as Subject Expert of UGC Interview Panel.
- 5) Worked as invited reviewer and examiner for PhD Defense, in Rashtrasant Tukadoji Maharaj Nagpur University.
- 6) Working as a member of Department Advisory Board (Electronics Department).
- 7) Worked as Session Track Coordinator in ICCICT 2015, 2018.
- 8) Worked as organizing committee member of FDP at SPIT in May 2015.
- 9) Worked as Joint Chief Conductor for university examination at DJSCOE Mumbai.
- 10) Worked as a member of Syllabus Setting committee for Mechatronics Course (Subject: Power Electronics & Drives) in Mumbai University.
- 11) Worked as member of Curriculum Development Committee for Autonomy of the Institute.
- 12) Worked as Member of Interview Panel for selection of the Best Student in SPIT 2015-16.

- 13) Worked as Coordinator for final year projects in Electronics Department.
- 14) Worked as Examination Coordinator for Electronics Department.
- 15) Worked as Time-Table coordinator for Electronics Department.

Workshops/ FDP/ STTP attended:

1. Two week ISTE approved STTP on ‘Mastering Matlab’ at DMCOE, Airoli from 1st to 11th January, 2008.
2. One week “International Clinical Engineering Workshop at COEP Pune, from 20th to 25th February, 2011
3. Two week ISTE approved STTP on Basic Electronics at VNIT from 28th June to 8th July 2011.
4. Two week lecture series on Communication RF and Microwave Technology under TEQIP at VNIT Nagpur from 1st May to 11th May 2013.
5. AICTE approved two week FDP on “Electronic System Design: From Devices to Applications, at SPIT Mumbai, from 4th May to 15th May 2015.

Academic Achievements:

- GATE Qualified (GATE 2009)

Domain expertise and areas of interest:

- Biomedical Electronics
- Power Electronics
- Artificial Intelligence

PERSONAL INFORMATION

Born: June 1st, 1975 in Miraj (India)

Citizenship: Indian

Marital status: married, 2 kids

Contact Information

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Navi Mumbai, 410210
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rajendrasutar73@gmail.com