



Nikunj Parikh

Assitnt Professor, Electronics Department,
Sardar Patel Institute of Technology

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Contact:

Email id: nikunj_parikh@spit.ac.in

Mobile:: +91-9022-808-014

- **Date of joining the present institute: 02 July 2018**
- **Date of joining the present department: 02 July 2018**

▪ **Scholastic record:**

S.No	Degree	Institute	Year of passing	Class/CGPI/percentage
1	ME	SFIT,Mumbai	2016	8.53
2	BE	VIT,Wadala,Mumbai	2012	63.72%(Average)

• **Teaching/Industry experience in totality :**

1) Teaching : 4 Years

2) Industry: NIL

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▪ **Achievements:**

▪ **Awards:**

▪ **Domain expertise and areas of interest:**

Integrated Circuits, VLSI, Image Processing, Electric Networks

▪ **Details of subjects taught:**

S.No.	Name of subject	Year and branch
1.	Circuit Theory	SE Electronics Engineering
2.	Robotics	BE Electronics Engineering
3.	Computer Organization	SE Electronics Engineering
4.	Power Electronics	BE Electronics Engineering

▪ **Workshops/Refresher courses/Training programs/Seminars attended:**

1.) 1 week STTP on front end VLSI Design during 24th June to 29th June 2019 at Sardar Patel Institute of technology

▪ **Workshops/Refresher courses/Training programs/Seminars conducted as a resource person:**

NA

• **Membership of professional bodies:**

NA

▪ **Publications:**

International Conferences

S.No	Publisher	Title of International conference	Title of paper/article/poster presentation	Schedule and venue of conference
1.	1	ICICT	No Reference Image Quality Assessment using Extreme Learning Machine	August 2016, Coimbatore, India

▪ **Details of National and International Journal publications:**

S.No	Title of Journal	Title of paper	Co-authors
1.			
2.			
3.			
4.			

▪ **Details of Book Chapters Published: NA**

▪ **R& D and Consultancy: NA**

▪ **Contribution to Corporate life and management of the Department and Institution: NA**

- **Other Contributions: NA**