

Name of the Organizing Faculty	Details of the FDP/STTP/Workshop conducted by Electronics Department S.P.I.T	
	STTP/ FDP /Workshop	Date
Prof. P. V. Kasambe	AICTE Sponsored One week STTP on "Outcome Based Education (OBE): Critical Issues and Solution (A Case Study Approach): Implementation of Outcome Based Education (OBE) for Holistic Development"	4 th Jan 2021 to 8 th Jan 2021
Prof. P. V. Kasambe	AICTE Sponsored One week STTP on "Outcome Based Education (OBE): Critical Issues and Solution (A Case Study Approach): Philosophy and Design of Outcome Based Education (OBE)"	30 th Nov to 5 th Dec, 2020
Dr. S. S. Rathod	AICTE Sponsored TWO Week FDP on "Insights into Intelligent Automation, Machine Learning and Data Science: Data Science & Its Applications"	7 th Dec 2020 to 19 th Dec 2020
Dr. S. S. Rathod	AICTE Sponsored TWO Week FDP on "Insights into Intelligent Automation, Machine Learning and Data Science: Artificial Intelligence & Machine Learning"	17 th Nov 2020 to 29 th Nov 2020
Dr. S. S. Rathod	AICTE Sponsored Two Week FDP on "Insights into Intelligent Automation, Machine Learning and Data Science: Foundation of Data Science"	19 th Oct 2020 to 31 st Oct 2020
Dr. S. S. Rathod	AICTE Sponsored One Week Short Term Training Program on "VLSI Design Using Cadence Tools: Mixed Signal VLSI Design"	30 th November to 5 th December 2020
Dr. S. S. Rathod	AICTE Sponsored One Week Short Term Training Program on "VLSI Design Using Cadence Tools: Analog CMOS VLSI Design"	2-7 th November 2020

Dr. S. S. Rathod	AICTE Sponsored One Week Short Term Training Program on "VLSI Design Using Cadence Tools: Digital CMOS Design and Verification"	12-17 October 2020
Dr. S. S. Rathod	AICTE sponsored two week FDP on "Consumer Electronic Product Design, Testing, Reliability and Patenting"	1 st July to 13 th July 2019
Dr. S. S. Rathod	AICTE sponsored one week STTP on "Front End VLSI Design and Verification"	One week (6 days from 24 th -29 th June 2019)
G T Haldankar	Moodle Learning Management System organised by S.P.I.T and IITB	One day (1 st March 2019)
G T Haldankar	One day workshop on "Python"	22 nd June 2019
G T Haldankar	One day workshop on "SCILAB"	4 th May 2019
G T Haldankar	One day workshop on "Moodle Learning Management System" for teaching and nonteaching staff	15 th March 2019
Dr. S. S. Rathod	AICTE- ISTE approved even Days on "InnovativeTeaching Learning Practices to Achieve Outcome Based Education and Accreditation"	Seven days (3 rd May - 9 th May 2018)
Dr. S. S. Rathod	Two week workshop on "CMOS, Mixed signal and Radio Frequency VLSI Design" Organised by S.P.I.T and IIT Kharagpur	30 th Jan- 4 th Feb 17 (One Week)