



Bharatiya Vidya Bhavan's
Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (W.), 400 058.

Electronics Engineering Department

Academic Year: 2019-2020

Summary of Guest Lectures

Sr. No.	Gap	Topic	Speaker	Date	No. of student present	Relevance of POs, PSOs
1	Awareness about recent trends in VLSI	One day Workshop on VLSI	Dr. Ashwini Gajrushu Research Scholar, IITB, Mumbai	6th October 2019	10 (B.E. ETRX)	PO12
2	Knowledge of state space analysis and recent trends in control system	"State Space Analysis and Advances in control system"	Prof. D. S. Sawant Assistant Professor, EXTC department, MPSTME, Mumbai	26 th February 2020	65 (S.E ETRX)	PO2
3	Alumni talk	SPIT to University of Michigan and Research in Solid-State Physics and Medical Imaging	Ms. Aunnasha Sengupta Ph.D candidate at University of Michigan	13 th April 2020	49 (S.E, T.E, B.E ETRX)	PO12
4	Alumni talk	Electronics in Automotive Industry	Ms. Natasha Choudhary L&T Technology Services Infotainment Testing (Automotive OEM) Munich, Germany	14 th April 2020	37 (S.E, T.E, B.E ETRX)	PO12
5	Alumni talk	Being an IC Design Engineer	Mr. Henil Langalia Senior IC Design Engineer at Analog Devices	15 th April 2020	45 (S.E, T.E, B.E ETRX)	PO12
6	Alumni talk	General Q/A and Real-Time System Security	Mr. Tanmaya Mishra Ph.D. candidate at Virginia Tech	15 th April 2020	42 (S.E, T.E, B.E ETRX)	PO12
7	Alumni talk	HDL workflows with MATLAB	Mr. Pawan Fakatkar Education Technical Evangelist, MathWorks	16 th April 2020	38 (S.E, T.E, B.E)	PO12



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					ETRX)	
8	Alumni talk	GRAKN: The knowledge Graph software for Robot Operating System	Mr. Shreyas Palande M.S. Student at Technische University Delft, Netherlands and Mr. Niket Shah M.S. Student at University of Maryland	17 th April 2020	34 (S.E, T.E, B.E ETRX)	PO12
9	Alumni talk	ASIC verification current trends and methodologies	Mr. Nandish Avlani Senior Design Engineer at AMD	18 th April 2020	36 (S.E, T.E, B.E ETRX)	PO12
10	Alumni talk	General QnA on MS in US and Internet of Things	Ms. Aditi Tripathi PhD student at University of Florida	19 th April 2020	32 (S.E, T.E, B.E ETRX)	PO12
11	Alumni talk	Working as a verification engineer	Ms. Jaskirat Kaur SOC Verification Engineer, Apple	20 th April 2020	29 (S.E, T.E, B.E ETRX)	PO12
12	Alumni talk	Experience of working at Intel and ARM	Mr. Omkar Mansata Senior Design Engineer at ARM, Austin	21 st April 2020	31 (S.E, T.E, B.E ETRX)	PO12
13	Alumni talk	Open-Source tools for hardware and firmware design	Dr. Ronak Gandhi JMA Wireless, Sr. Electrical Engineer, Ph.D in Electrical and Computer Science from Syracuse University	22 nd April 2020	24 (S.E, T.E, B.E ETRX)	PO12



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14	Alumni talk	Pursuing non-technical education in the US and associated careers	Mr. Jugal Gala M.S. in Management Science & Engineering, Stanford	23 rd April 2020	20 (S.E, T.E, B.E ETRX)	PO12
15	Alumni talk	Insights into Digital Strategy and Brand Strategy	Ms. Samiksha Gupta Ex- Digital Strategy consultant with Deloitte and currently working as Brand Strategist at Leo Burnett.	24 th April 2020	15 (S.E, T.E, B.E ETRX)	PO12
16	Alumni talk	10 key learnings in my journey from Campus to Corporate to Entrepreneurship	Mr. Rohan Daftary Core Member & Co-Founder, Fintso; MBA - IIM Trichy. Ex Avendus, Kotak	25 th April 2020	17 (S.E, T.E, B.E ETRX)	PO12
17	Alumni talk	Experience in Front End verification at PowerVR Graphics Processor Division, Imagination Technologies	Mr. Jugal Mistry Senior Hardware Engineer, Imagination Technologies, Pune	26 th April 2020	29 (S.E, T.E, B.E ETRX)	PO12
18	Alumni talk	2 cents on navigating through life in the US, Any general EE/VLSI related questions	Mr. Karan Shah MS in Electrical Engineering from USC, Memory Systems design engineer at Western Digital Ms. Gauri Dalvi MS in Electrical Engineering from USC, Physical Design engineer at NVIDIA Corporation	26 th April 2020	29 (S.E, T.E, B.E ETRX)	PO12
19	Alumni talk	A Day in Life of Physical Design	Mr. Nimish Agashiwala Graphics Hardware	27 th April 2020	25 (S.E,	PO12



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		Engineer + MS VLSI Queries	Engineer, Intel Corporation, Folsom, CA		T.E, B.E ETRX)	
20	Alumni talk	Industry Standard VLSI tool flow methodology (specifically Auto Place and Route)	Mr. Ameya Abhyankar SoC Design and Methodology Manager, Intel Corporation, Portland, Oregon	27 th April 2020	25 (S.E, T.E, B.E ETRX)	PO12
21	Alumni talk	Experience of working on Embedded Systems at CISCO and building RTOS	Mr. Kshitij Desai Software Engineer at Netskope, San Francisco	28 th April 2020	20 (S.E, T.E, B.E ETRX)	PO12
22	Alumni talk	Q/A Performance modelling, Compilers, NVIDIA	Mr. Kartik Haria System Software Engineer, NVIDIA, Santa Clara	1 st May 2020	22 (S.E, T.E, B.E ETRX)	PO12
23	Alumni talk	Research at UBC	Prof. Prashant Nair University of British Columbia UK	2 nd May 2020	26 (S.E, T.E, B.E ETRX)	PO12
24	Alumni talk	Implementing User Research, Lean Startup, and Product Management in academic projects	Mr. Prasad Gaikwad Masters in Mechanical Engineering (Product Design) UC Berkeley, Product Manager (Siemens Pittsburgh)	2 nd May 2020	26 (S.E, T.E, B.E ETRX)	PO12
25	Alumni talk	Introduction to Machine Learning and Basic Terminologies	Ms. Sanskruti Naik, Systems Engineer at Qualcomm, Mr. Nachiket Nanadikar Senior Embedded Software Engineer at	3 rd May 2020	22 (S.E, T.E, B.E ETRX)	PO12



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			Qualcomm			
26	Alumni talk	Flexible Stretchable Electronics	Mr. Virendra Parab PhD scholar at Indian Institute of Science, Bengaluru	3 rd May 2020	22 (S.E, T.E, B.E ETRX)	PO12
27	Alumni talk	Insights into Voice enabled embedded systems to Search Engines	Mr. Samarth Shah Software Developer, Microsoft, Redmond (Masters in Embedded Systems, UPenn)	8 th May 2020	70+ (S.E, T.E, B.E ETRX)	PO12
28	Alumni talk	Changing energy landscape (Renewables & beyond) + General chat on personal development	Mr. Srinath Iyer (Master in Management from The London School of Economics and Political Science, Masters in International Management Ivey Business School at Western University) Senior Business Developer, Gas to Power, BP	8 th May 2020	70+ (S.E, T.E, B.E ETRX)	PO12
29	Alumni talk	DFT and Scan Insertion	Ms. Aditi Samant CPU Design Engineer, Intel Corp., Hudson, Massachusetts	9 th May 2020	24 (S.E, T.E, B.E ETRX)	PO12
30	Alumni talk	Overview on Mixed Signal Design Trends and Aspects of Verification	Mr. Manish Jain ASIC Design Engineer, Apple	9 th May 2020	20 (S.E, T.E, B.E ETRX)	PO12
31	Alumni talk	From Electronics to Neuroscience	Ms. Brinda Sevak Research Specialist, Department of Neuroscience, University of Wisconsin – Madison	10 th May 2020	22 (S.E, T.E, B.E)	PO12



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32	Alumni talk	VLSI design job opportunities available in India. (About chip designing)	Mr. Saikalash Shetty Staff Engineer in Samsung Electronics	10 th May 2020	16 (S.E, T.E, B.E ETRX)	PO12
33	Awareness about possibilities of higher studies and opportunities in core domain.	"Career Options and Opportunities"	Mr. Renjith.C.V. Electrical Architect/Product Designer, Philips India Ltd., Pune	7 th June 2020	93 (T.E, B.E ETRX)	PO1, PO6, PO12
34	Awareness about care to be taken while designing medical devices.	"Medical Device Design"	Mr. Renjith.C.V. Electrical Architect/Product Designer, Philips India Ltd., Pune	21 st June 2020	43 (B.E)	PO1, PO6, PO12